NICKOLAS E. WESTMAN

JUDSON K. CHAMPLIN

STEVEN M. KOEHLER

THEODORE M. MAGEE

DEIRDRE MEGLEY KVALE

JOHN D. VELDHUIS-KROEZE

JOSEPH R. KELLY

DAVID D. BRUSH

Z. PETER SAWICKI

Telephone (612) 334-3222 Facsimile (612) 334-3312 VIDEO CONFERENCE (612) 333-4611

CHRISTOPHER R. CHRISTENSON BRIAN D. KAUL ALAN G. REGO CHRISTOPHER L. HOLT TODD R. FRONEK LINDA P. JI NATHAN M. RAU LEANNE R. TAVEGGIA PETER J. IMS BRYAN F. ERICKSON R. MICHAEL REED

> SENIOR COUNSEL ROBERT M. ANGUS DAVID C. BOHN

OF COUNSEL KATHRYN M. HOWARD

Express Mailing No. : EV388910761US Date of Deposit: April 1, 2004

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

New U.S. Patent Application of:

Applicant: Judy M. Gehman et al.

For SYSTEM AND METHOD FOR IMPLEMENTING MULTIPLE

INSTANTIATED CONFIGURABLE PERIPHERALS IN A

CIRCUIT DESIGN

Our File: 113.12-0258

Dear Sir:

Enclosed for filing are the following papers in connection with the above-identified patent application:

1. Complete specification and claims.

49 pages Specification

6 pages claims

1 page Abstract

Unexecuted Combined Declaration and Power of Attorney (3 pages).

5 sheets of drawings.

Application Data Sheet (3 pages)

The filing fee is not enclosed with this communication. Pursuant to 35 USC § 111 and 37 CFR §§ 1.53(b) and 1.53(f), the filing fee, executed Declaration and executed Verified Statement Claiming Small Entity Status (if applicable) will be filed separately.



A filing date under 37 CFR §§ 1.10(b) and 1.53(b) of <u>April 1</u>, 2004 is respectfully requested. The enclosed materials are being sent "Express Mail Post Office to Addressee" as of the date of this letter.

Yours very truly,

Theodore M. Magee Reg. No. 39,758

TMM:slg Enc.

PATENT APPLICATION OF

JUDY M. GEHMAN, 3736 ROCHDALE DRIVE, FT. COLLINS, COLORADO 80525, CITIZENSHIP: USA; MATTHEW D. KIRKWOOD, 174 SOUTHWIND LANE, WEST ST. PAUL, MINNESOTA 55118, CITIZENSHIP: USA; AND STEVEN M. EMERSON, 8409 STONE CREEK COURT, CHANHASSEN, MINNESOTA 55317; CITIZENSHIP: USA

ENTITLED

SYSTEM AND METHOD FOR IMPLEMENTING MULTIPLE INSTANTIATED CONFIGURABLE PERIPHERALS IN A CIRCUIT DESIGN